

1/6

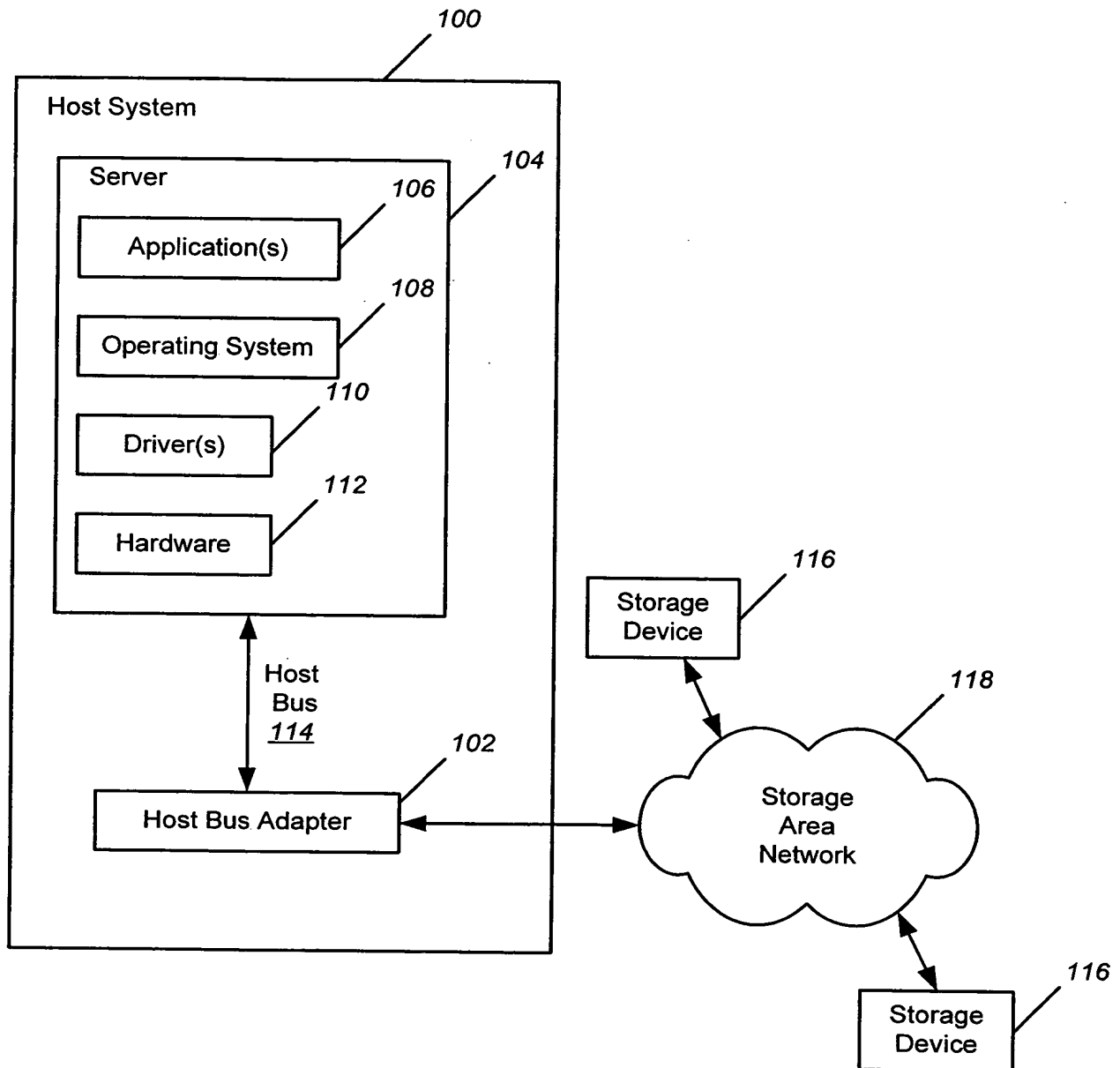


FIG. 1

2/6

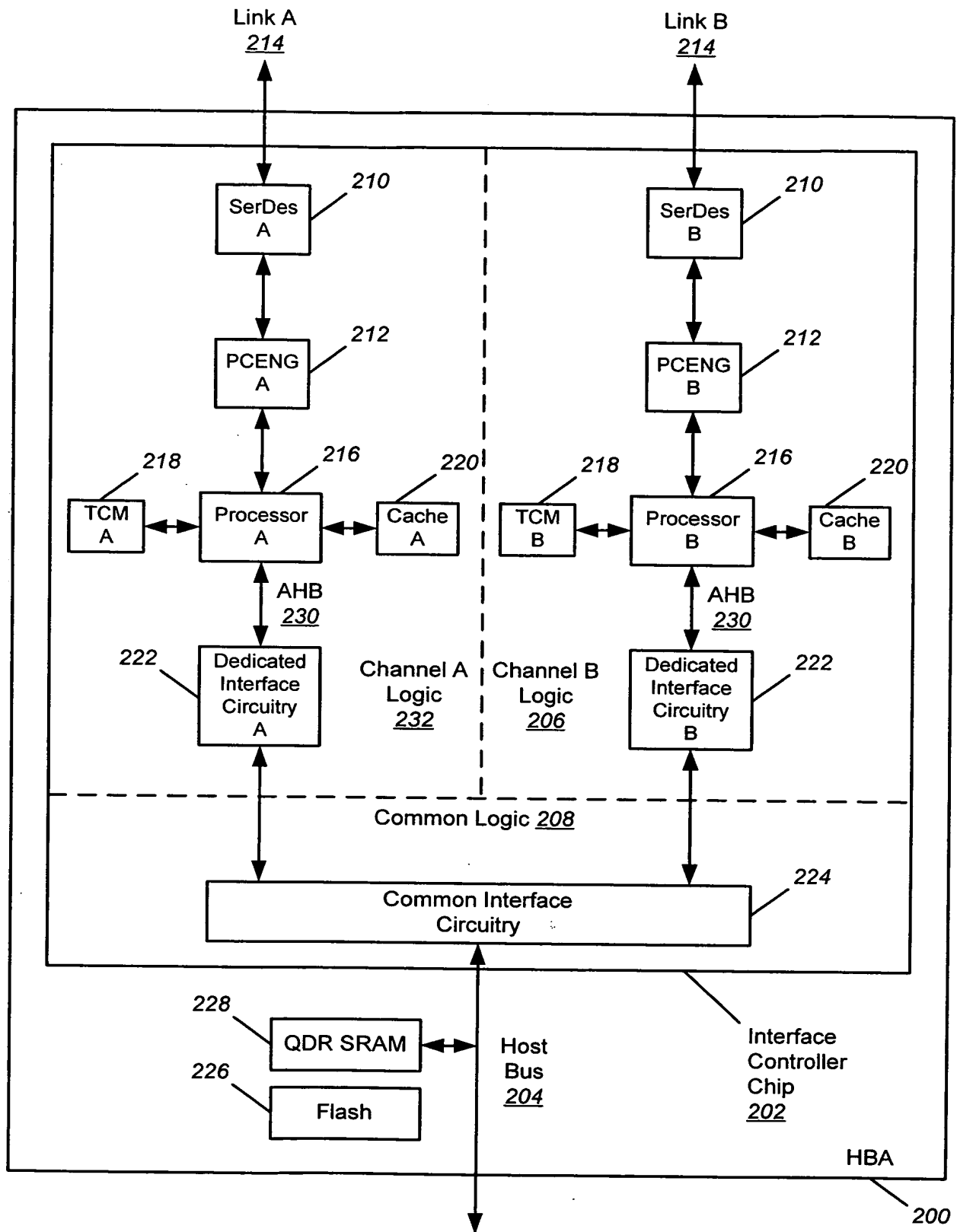


FIG. 2

3/6

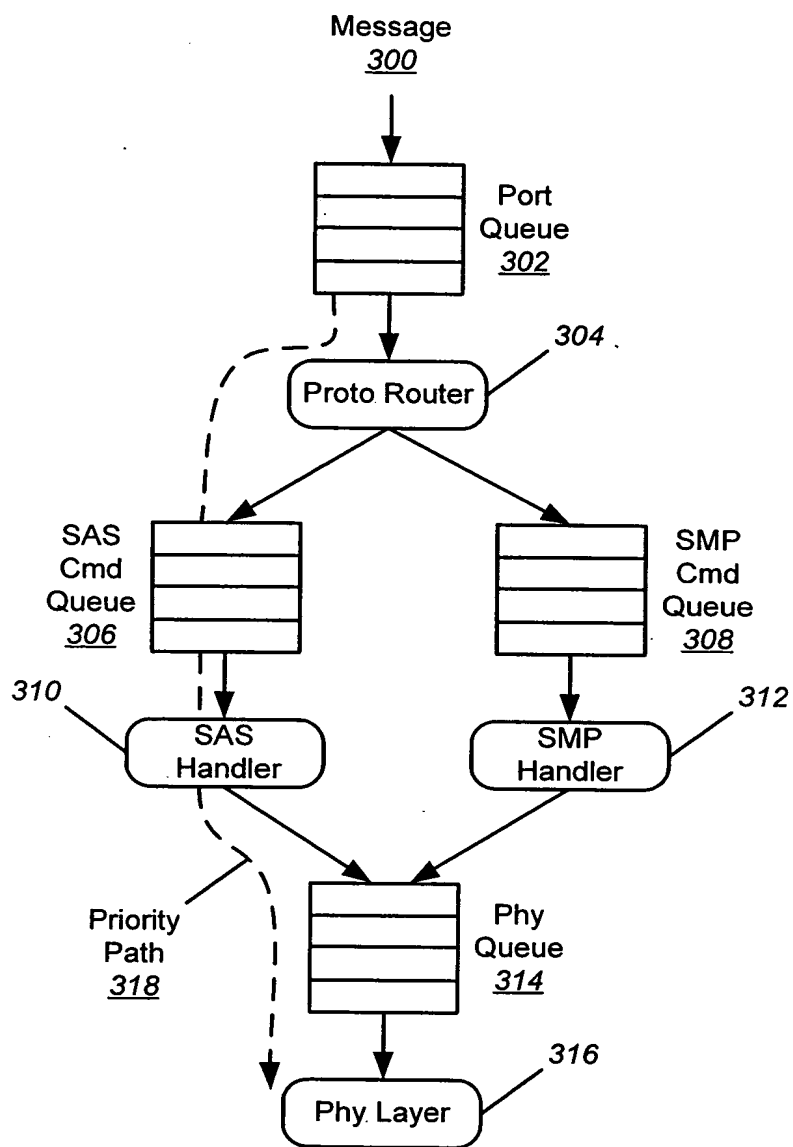


FIG. 3

4/6

	Port Queue		SAS Cmd Queue		SMP Cmd Queue		Phy Queue	
	!Full	!Empty	!Full	!Empty	!Full	!Empty	!Full	!Empty
Proto Router	0	1	1	0	1	0	0	0
SAS Handler	0	0	0	1	0	0	1	0
Phy Layer	0	0	0	0	0	0	0	1
SMP Handler	0	0	0	0	0	1	1	0
Idle Task	0	0	0	0	0	0	0	0

400

System State	1	0	1	1	1	0	1	0
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Empty Queue

Contains Message(s)

Empty Queue

Empty Queue

FIG. 4

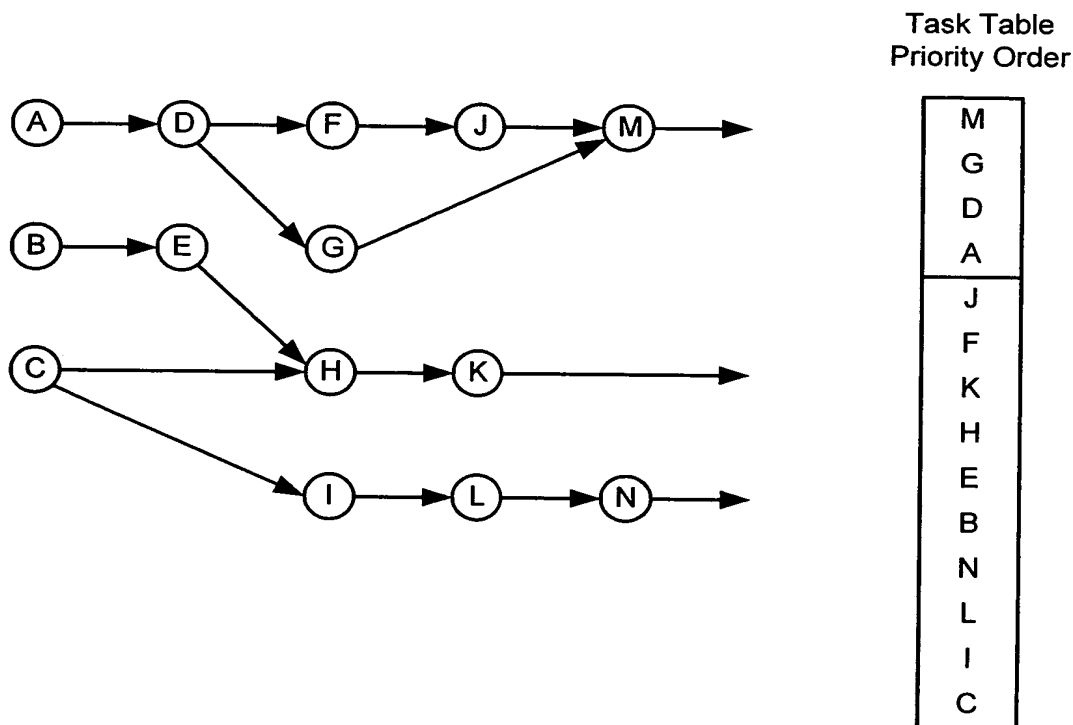


FIG. 5

5/6

Task	Prerequisites	Implied Priority	Block Priority
M		0	0
G		1	
D		2	
A		3	
J		4	1
F		5	
K		6	2
H		7	
E		8	
B		9	
...		10	

FIG. 6

Limit	Flags[0]	Flags[1]	Flags[2]	Flags[3]
0	X	O	O	O
2	O	X	X	O
1	X	X	O	O
3	X	O	O	X

FIG. 7

6/6

	Buffer Pool		Message Queue		SLI-2	Cmd TO
	!Full	!Empty	!Full	!Empty	On	Timed-Out
Task_0	0	1	0	0	0	0
Task_1	0	0	0	1	0	0
Task_TO	0	0	0	0	1	1
Idle	0	0	0	0	0	0

FIG. 8A

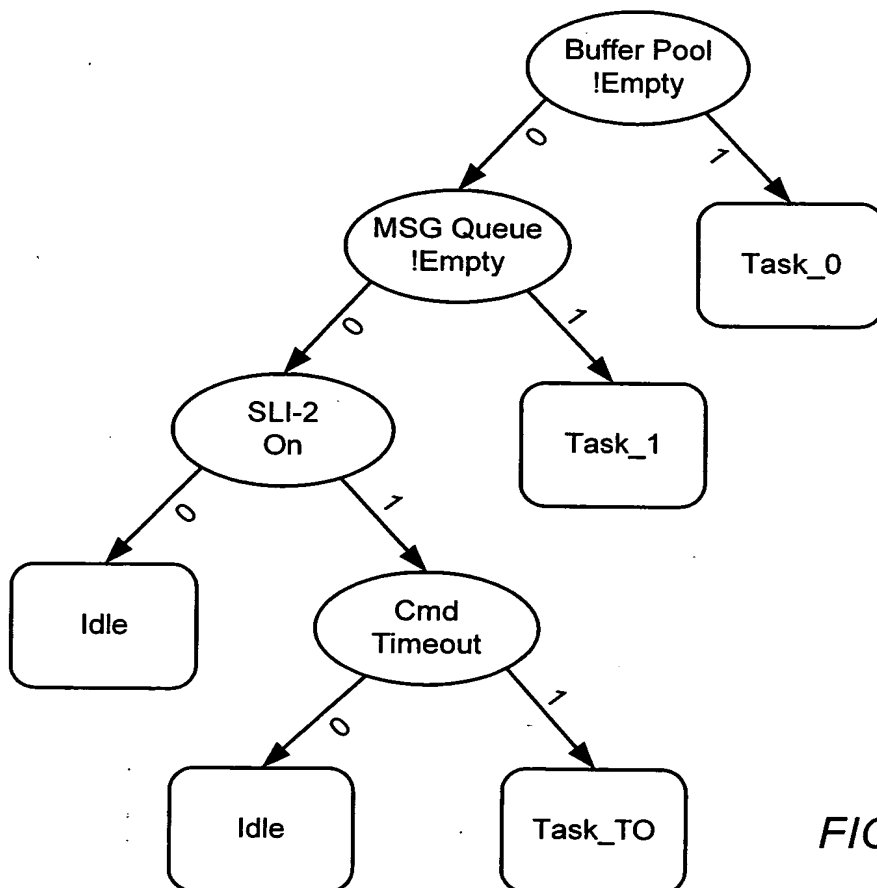


FIG. 8B